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APPLICANTS

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** CONTINUING DATA ***** *hu*

** FOREIGN APPLICATIONS ***** *ma*
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>[Signature]</i> Initials <i>[Initials]</i>	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 2
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TITLE
Semiconductor device including interconnects formed by damascene process and manufacturing method thereof

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